

**Notice of Allowability**

Application No.

10/719,031

Examiner

Stephen G. Sherman

Applicant(s)

KOYAMA ET AL.

Art Unit

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the Request for Continued Examination filed the 30 May 2007.
2. ☒ The allowed claim(s) is/are 1-4, 6-8, 13 and 15-38.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

## DETAILED ACTION

### *Allowable Subject Matter*

1. Claims 1-4, 6-8, 13 and 15-38 are allowed.
2. The following is an examiner's statement of reasons for allowance:

***Regarding independent claims 1-2 and 19-21***, the primary reason for allowance is the combination of the limitations "wherein at least one of the pixels comprises a first light emitting element, a second light emitting element, a first transistor and a second transistor", "wherein the first light emitting element comprises an electroluminescent layer between a first electrode and a second electrode", "wherein the second light emitting element comprises the electroluminescent layer between the first electrode and a third electrode" and "wherein the first transistor is electrically connected to the first light emitting element, and wherein the second transistor is electrically connected to the second light emitting element", which is not found singularly or in combination within the prior art.

The closest prior art reference, Kim (KR 2002-094423) shows a pixel configuration that includes a first electrode (12) and a second electrode (14) and an organic light-emitting layer (13) disposed between the first and second electrodes. Kim,

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however, does not show a first transistor electrically connected to a first light emitting element or a second transistor electrically connected to a second light-emitting element.

***Regarding independent claims 3-4***, the primary reason for allowance is the combination of the limitations “wherein a source signal line driver circuit, a first gate signal line driver circuit and a second gate line driver circuit are formed on the surface of the substrate on which the pixel portion is formed, wherein a scan direction of the first gate signal line driver circuit is orthogonal to that of the second gate signal line driver circuit” and “wherein the first light emitting element comprises an electroluminescent layer between a first electrode and a second electrode” and “wherein the second light emitting element comprises the electroluminescent layer between the first electrode and a third electrode” and “wherein the second light emitting element emits light in a second direction which is opposite to the first direction”, which is not found singularly or in combination within the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

**Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stephen G. Sherman whose telephone number is (571) 272-2941. The examiner can normally be reached on M-F, 8:00 a.m. - 4:30 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amr Awad can be reached on (571) 272-7764. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

SS

19 June 2007

AMR A. AWAD  
SUPERVISORY PATENT EXAMINER

